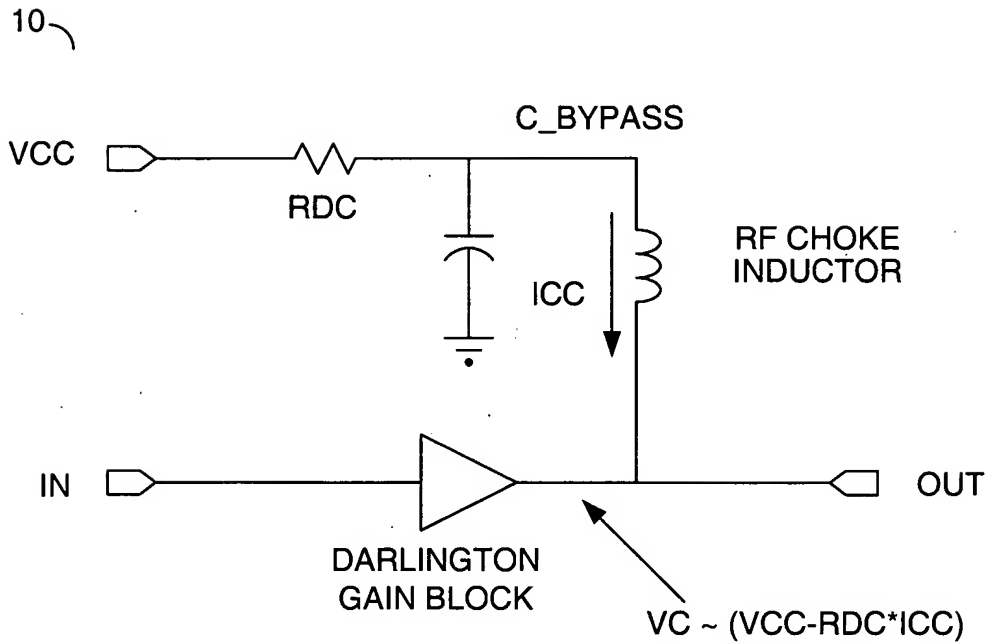




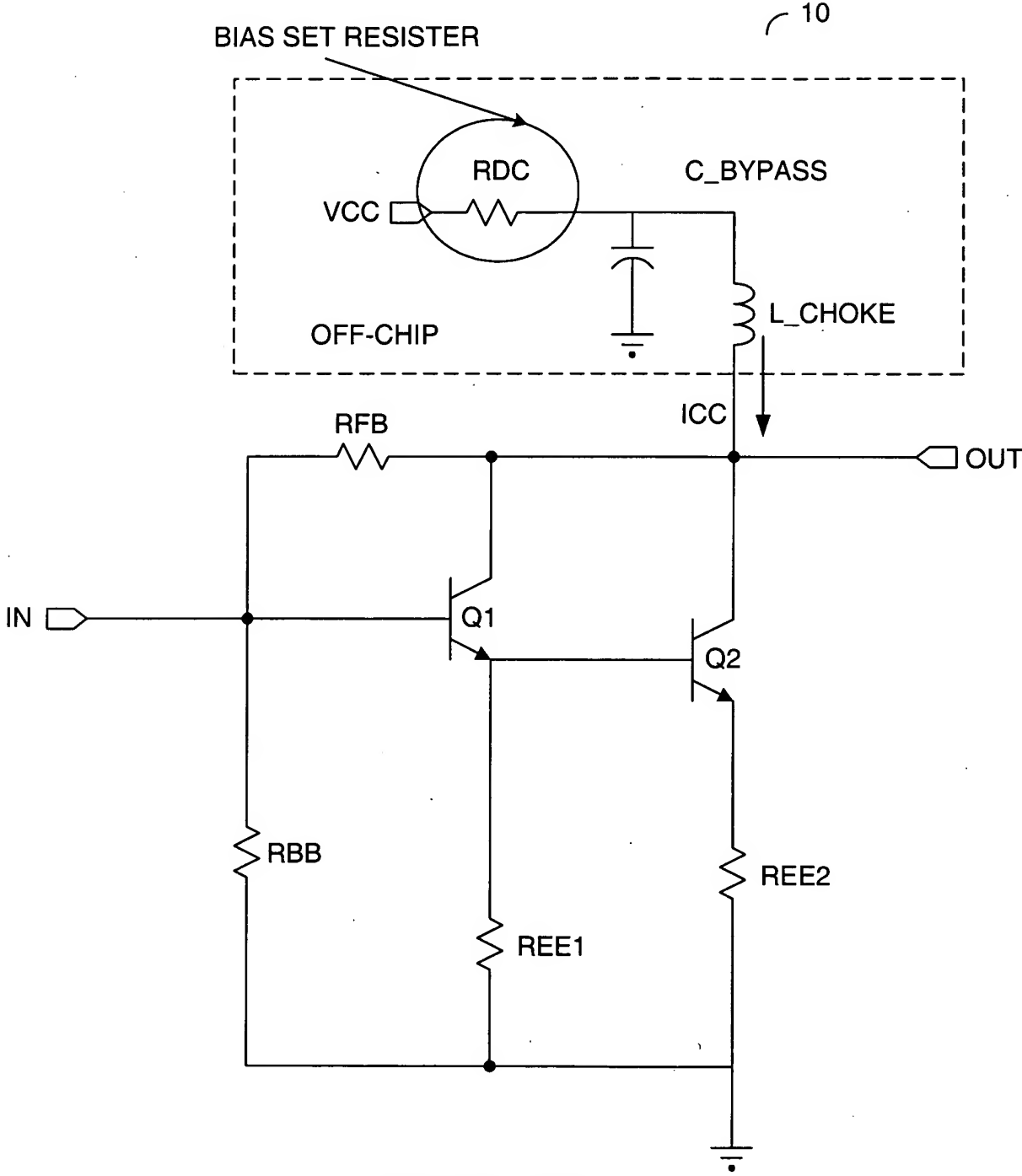
# REPLACEMENT SHEET

10/623,070  
2817



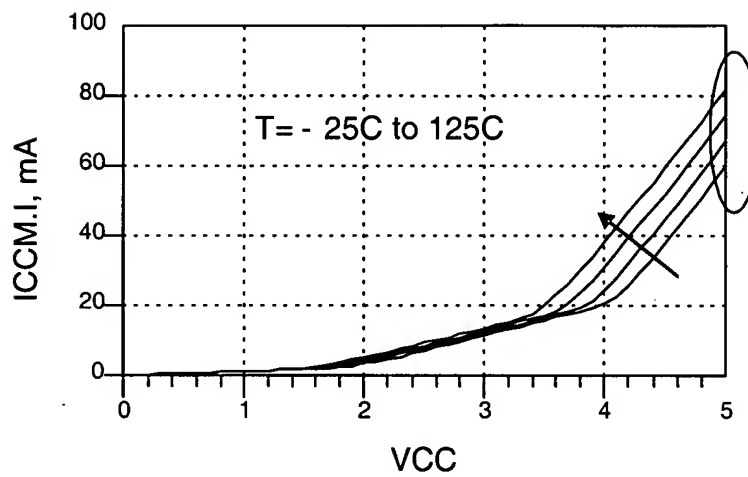
(CONVENTIONAL)

FIG. 1



(CONVENTIONAL)

FIG. 2

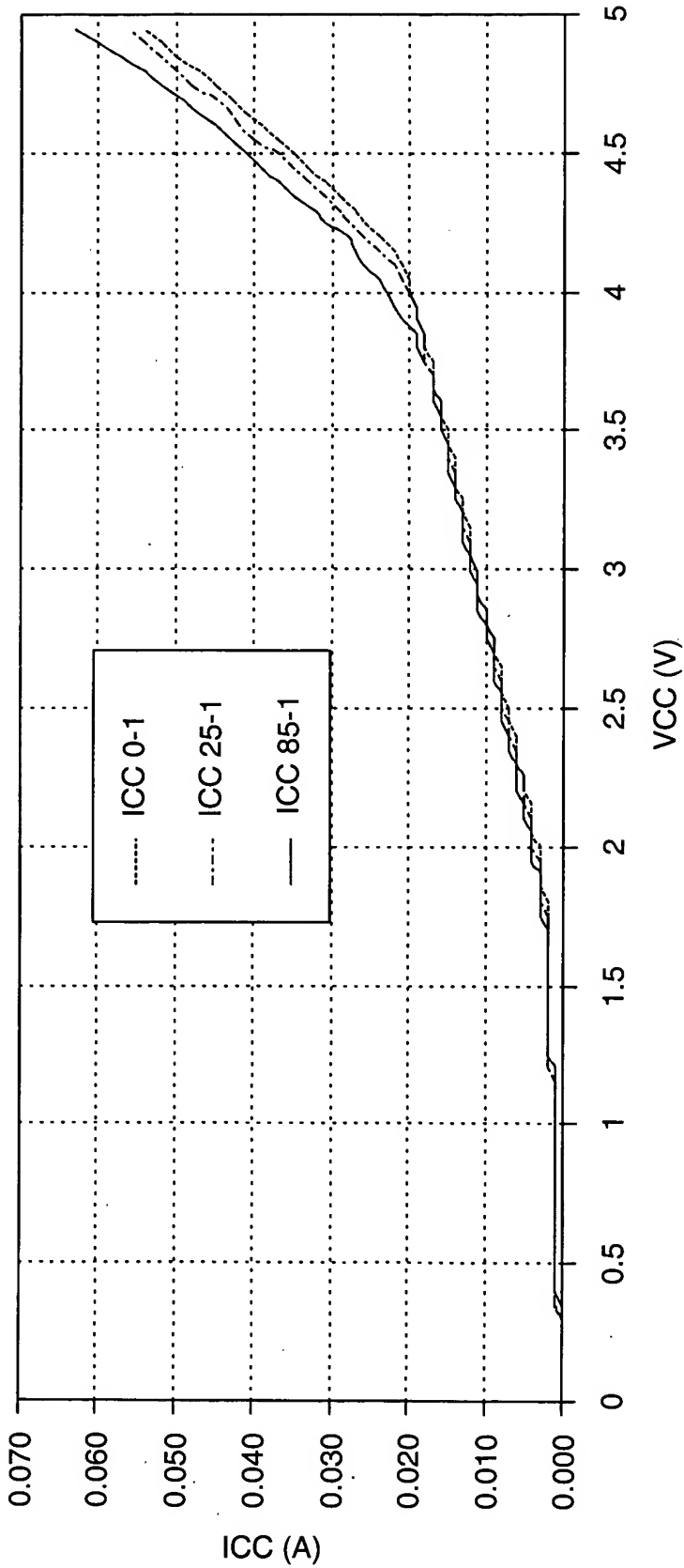


20 mA VARIATION OR +/- 14%

(CONVENTIONAL)

FIG. 3

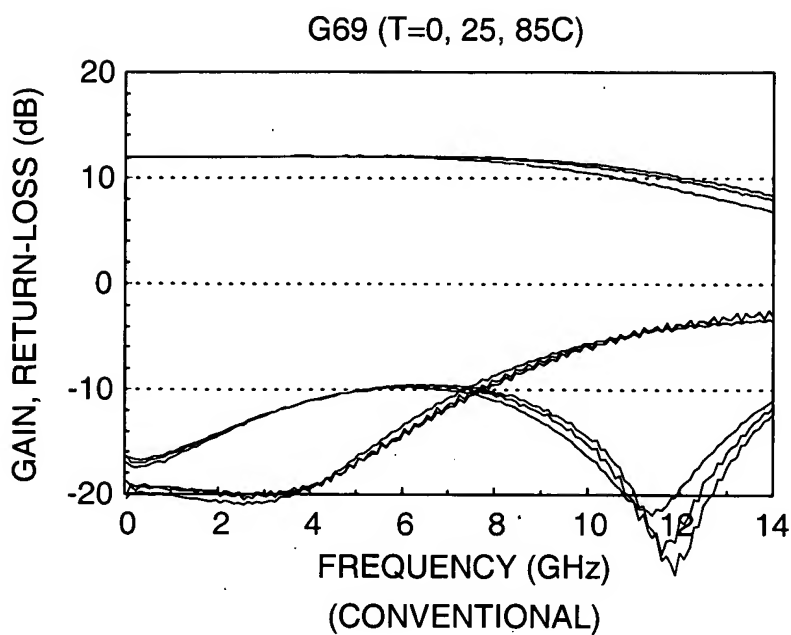
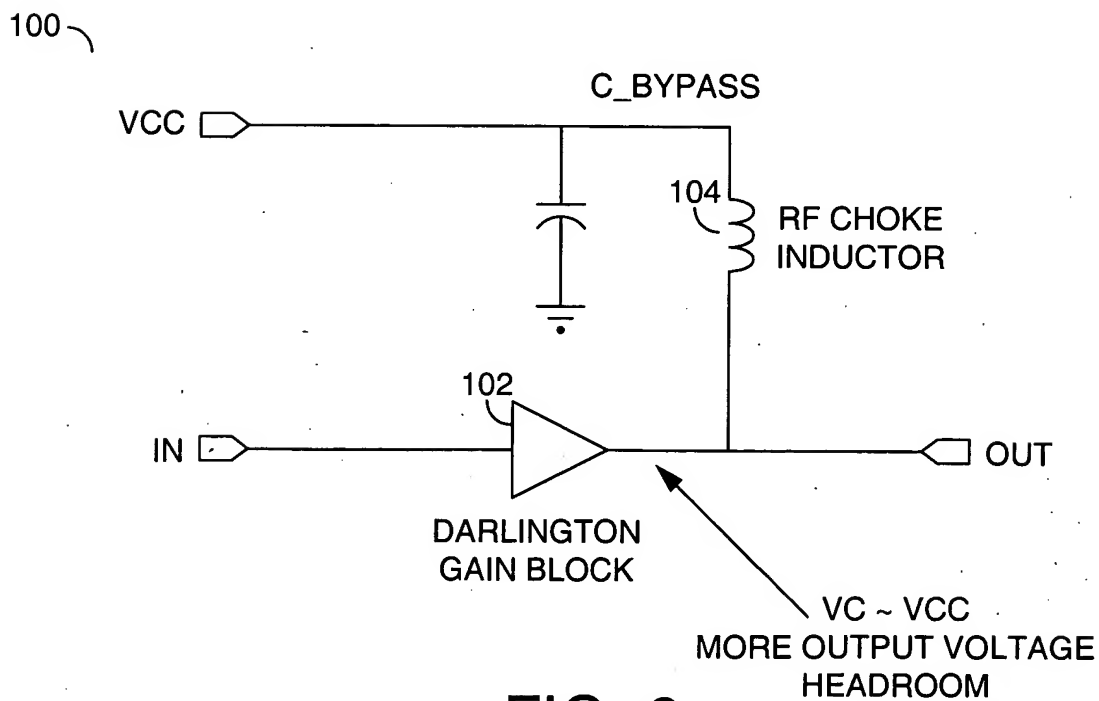
ICC VS VCC  
G69 (T=0, 25, 85C)

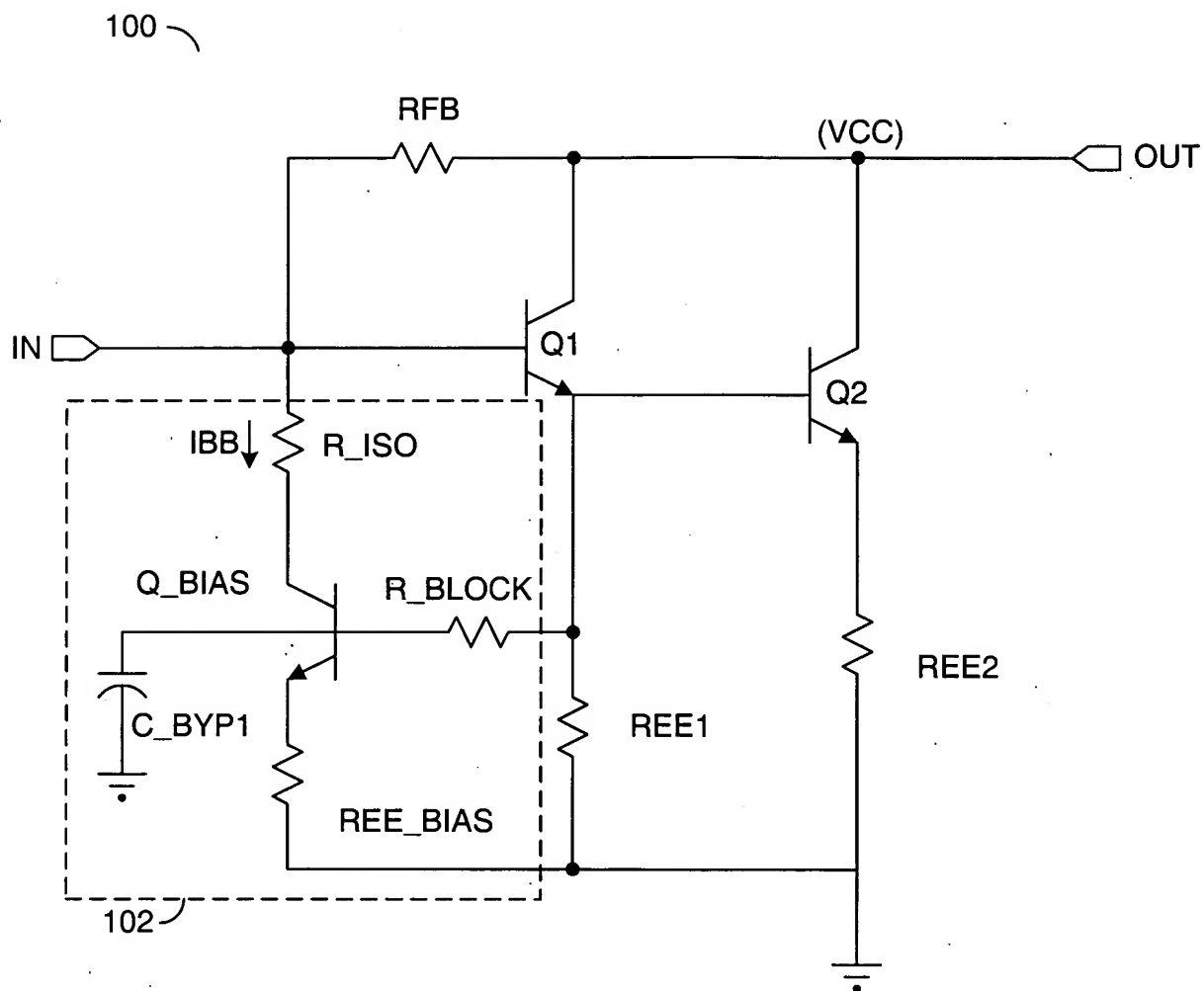


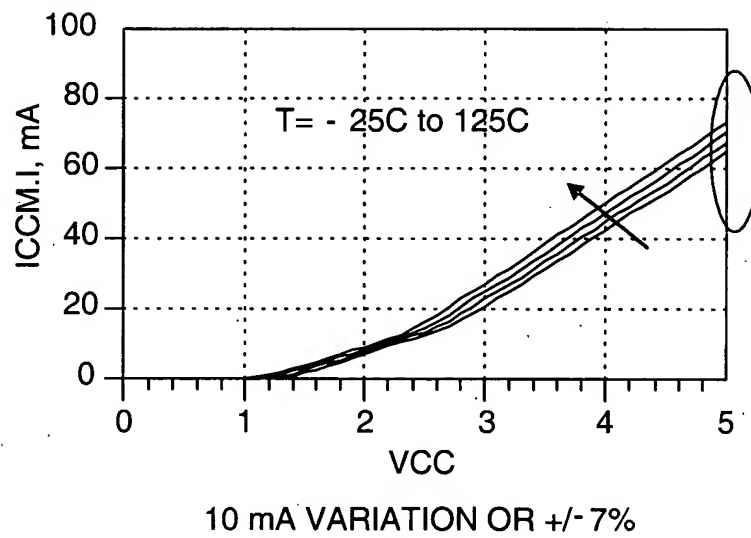
~8 mA VARIATION OVER TEMP  
(13% VARIATION, T=0 - 85C)  
~40 mA/V VCC SENSITIVITY

(CONVENTIONAL)

**FIG. 4**

FIG. 5FIG. 6

FIG. 7

**FIG. 8**



**FIG. 9**



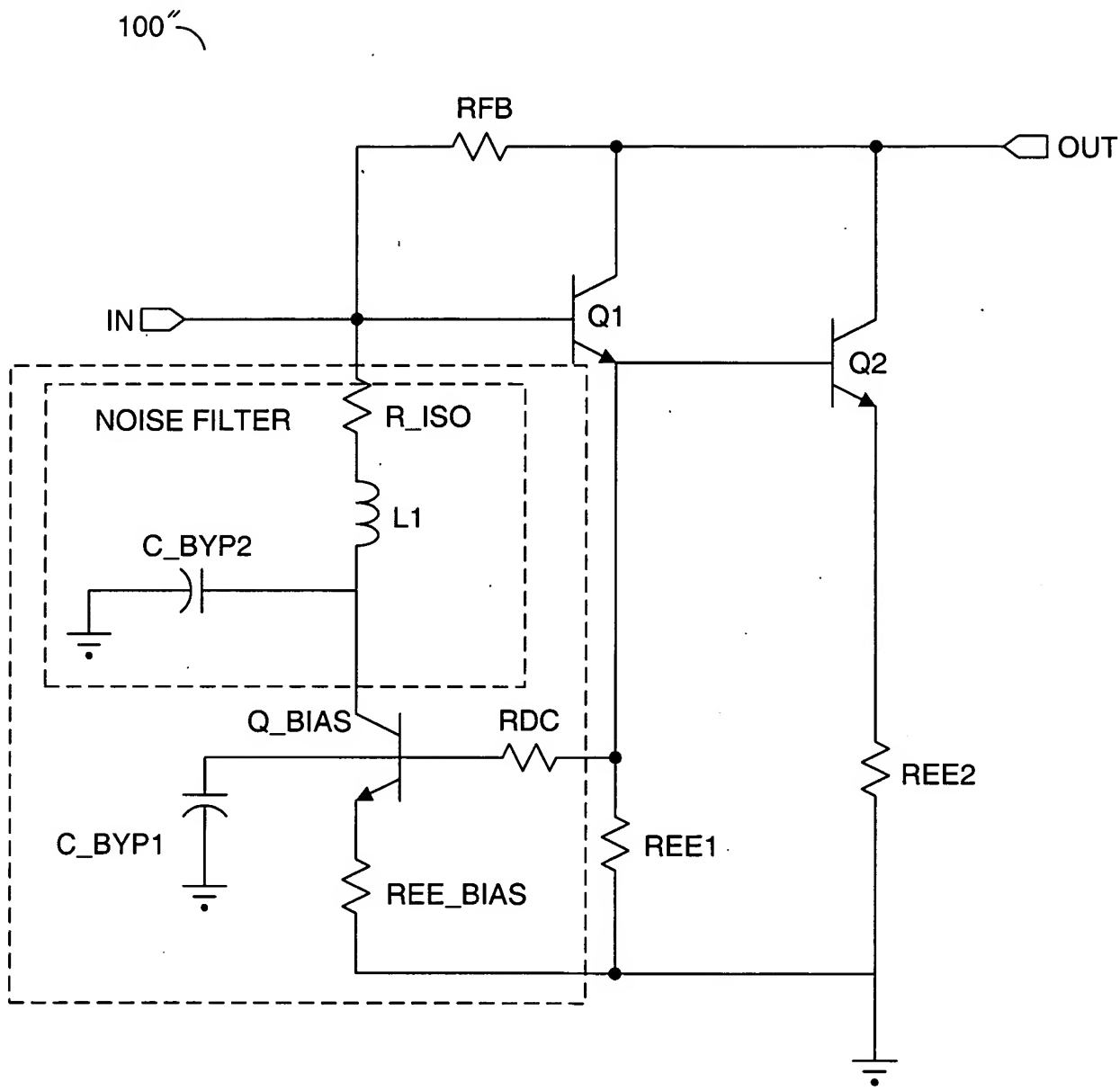
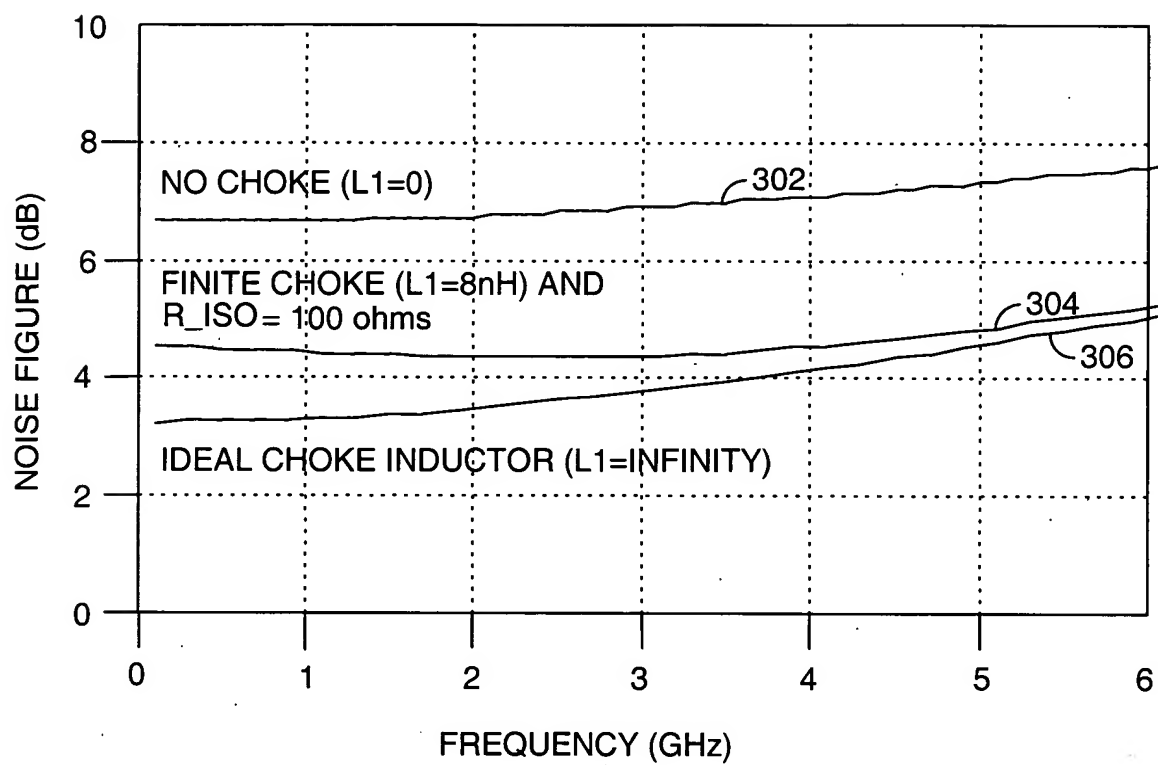


FIG. 10

FIG. 11

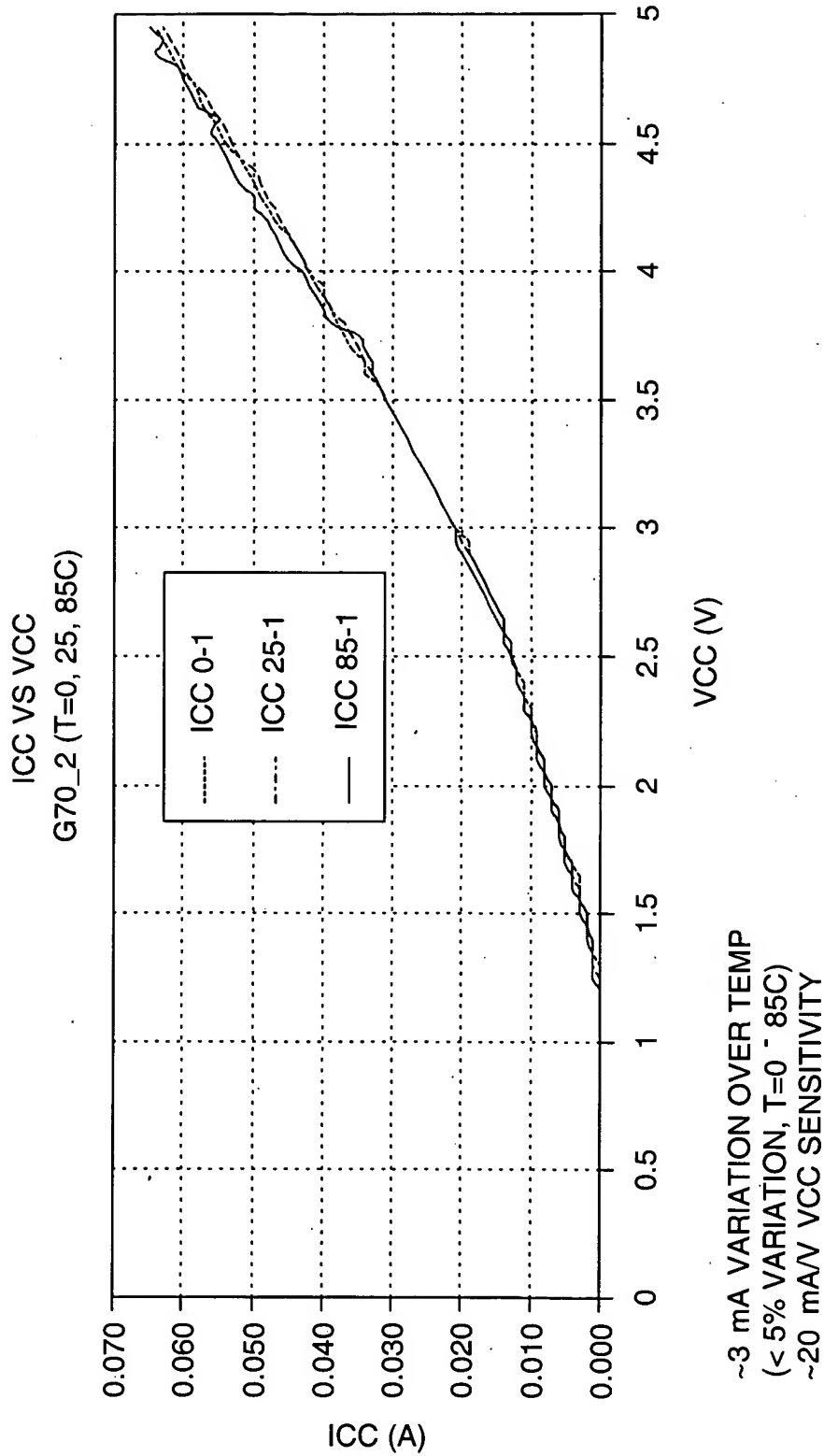
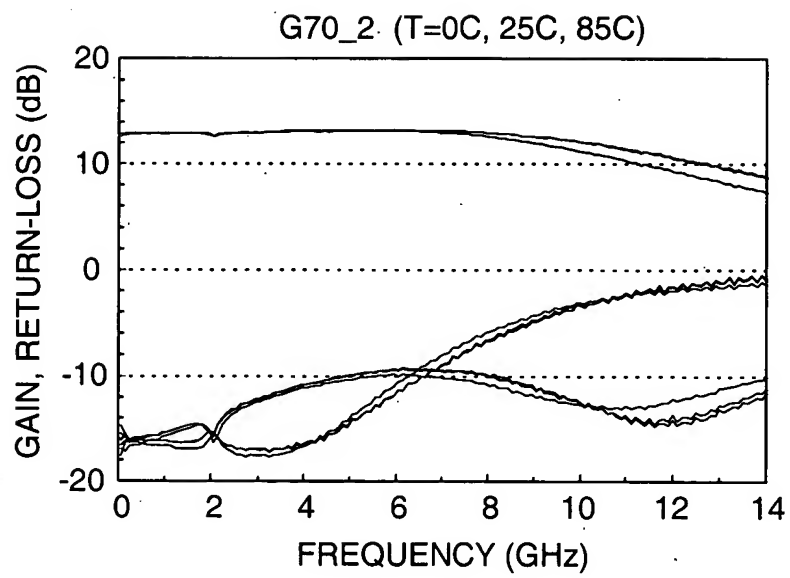
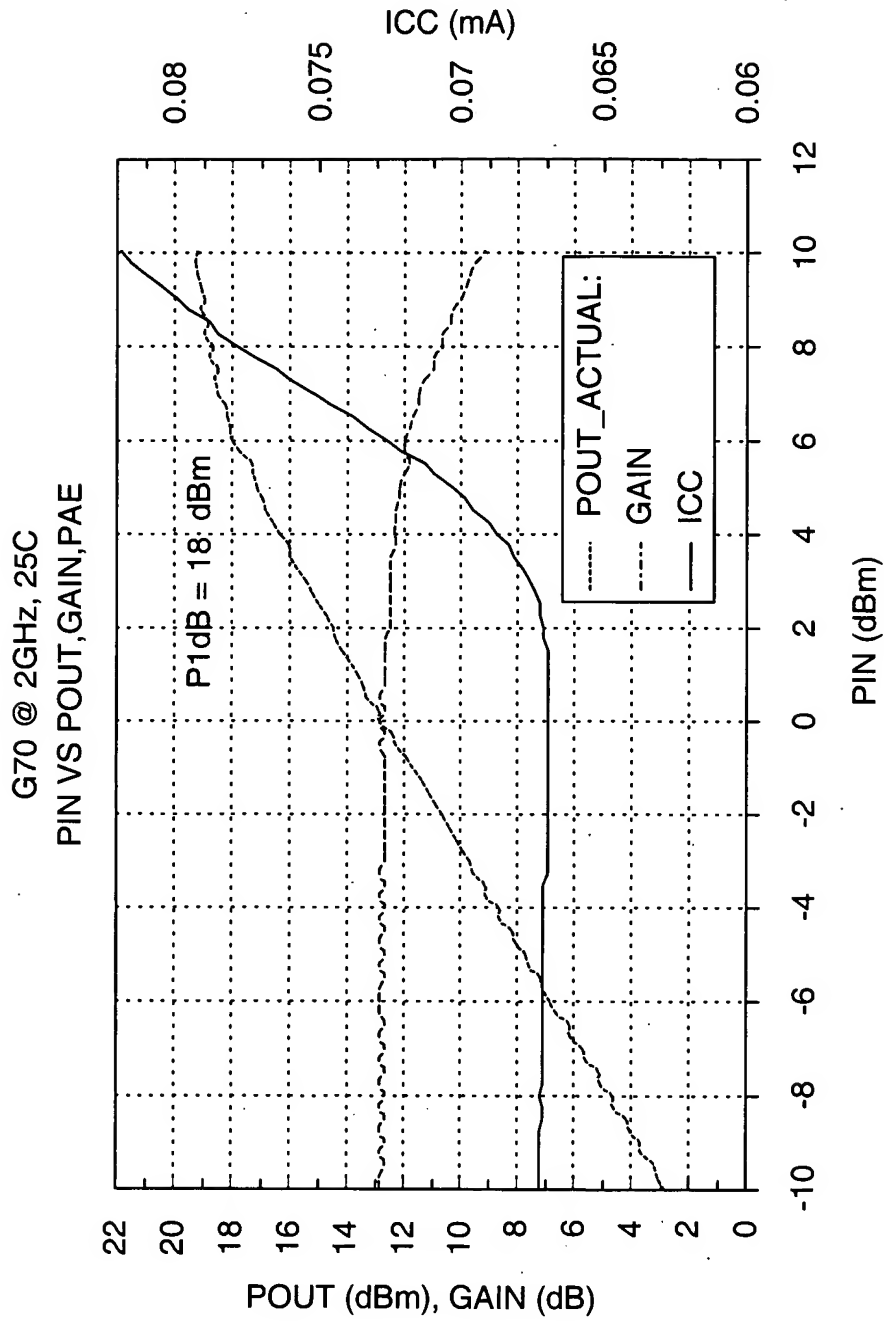


FIG. 12

FIG. 13

SELF- BIAS AMP (G70\_2) P1dB @ 2 GHz  
TEMP = 25C



SELF- BIAS ALLOWS ICC TO INCREASE WITH PIN →  
TOPOLOGY CAN BE DESIGNED FOR CLASS B, AB BIAS

**FIG. 14**